

8-PSK MODULATION/ DEMODULATION TRAINER

MODEL - PSK100-8

This trainer has been designed with a view to provide practical / experimental knowledge of 8-P.S.K. Modulation/Demodulation on a single PCB.



SPECIFICATIONS

- 1. Power supply requirement
- 2. Built in IC based power supply.
- 3. On Board Modulating Digital Data signal generator to generate any binary input word with Bit clock & Word clock.

230V AC, 50 Hz.

Word Length	:	8 Bits.
Word Clock Frequency	:	8 KHz to 12 KHz.
Data Format	:	NRZ & Bipolar NRZ

- Modulator Type : Balanced Modulator as Phase Modulator.
- 5. Demodulator Type : Balanced Modulator as Phase detector. Bit clock recovery section.
- 6. All parts are soldered on single PCB with complete circuit diagram Screen-printed.

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- 7. Standard Accessories : 1. A Training Manual
 - 2. Connecting Patch cords.

EXPERIMENTS

- 1. To study theory of PSK Modulation & Demodulation.
- 2. To generate PSK signal

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- 3. To demodulate PSK signal using balanced modulators.
- 4. To observe PSK modulated output by for different input binary codes.
- 5. To observe PSK modulated output by varying the bit clock frequency.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.